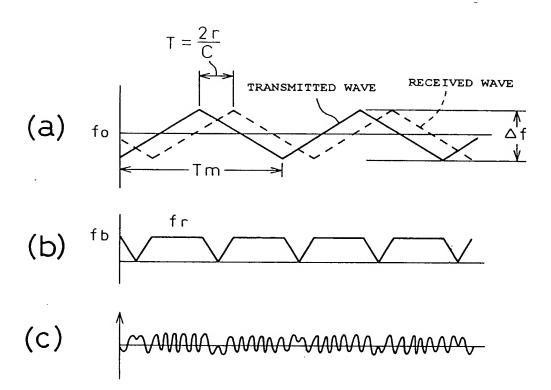
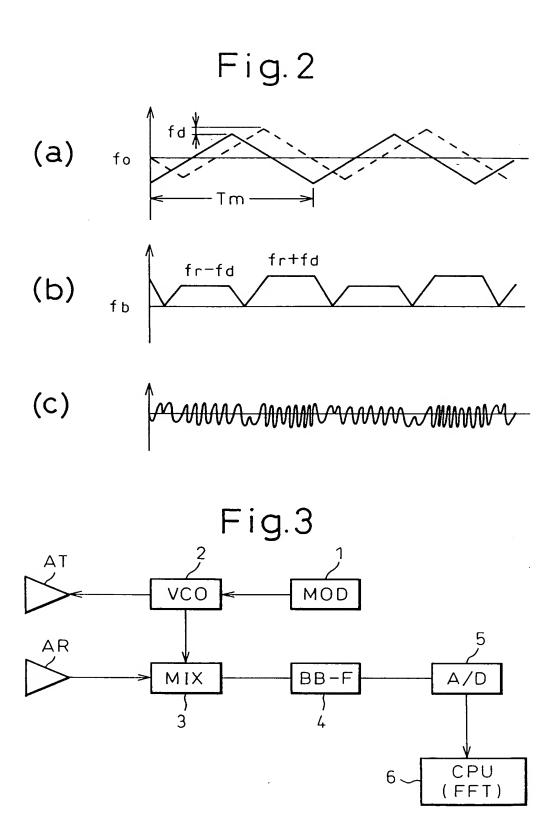
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Fig.1

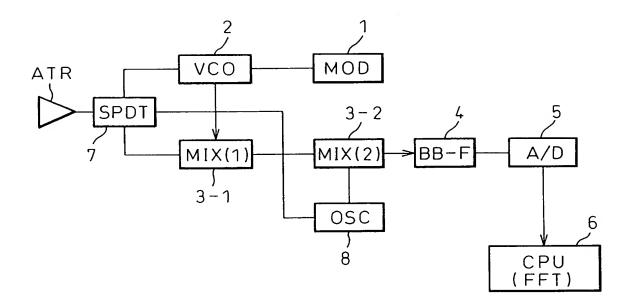


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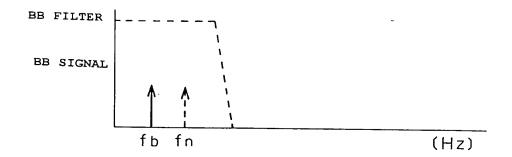
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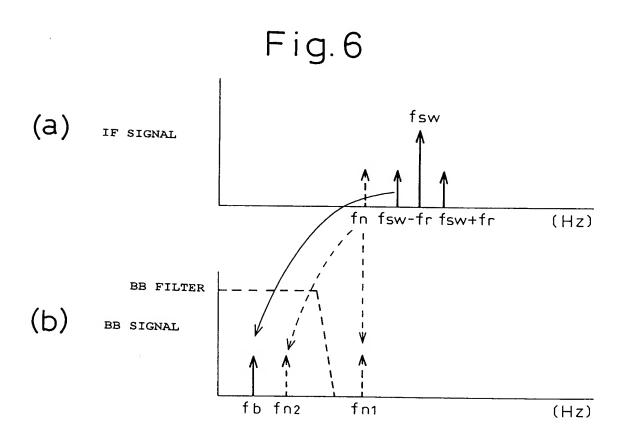
Fig.4



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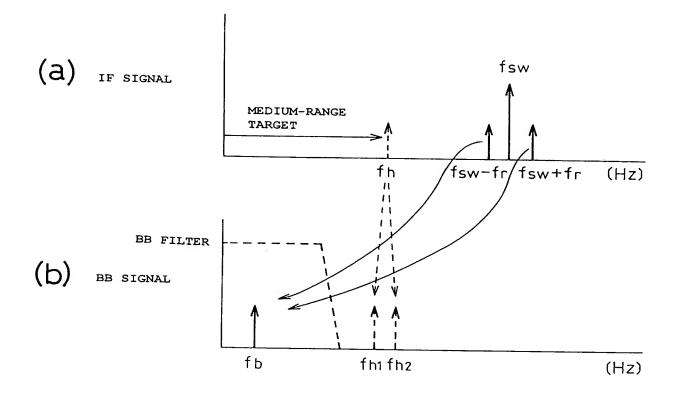
Fig.5





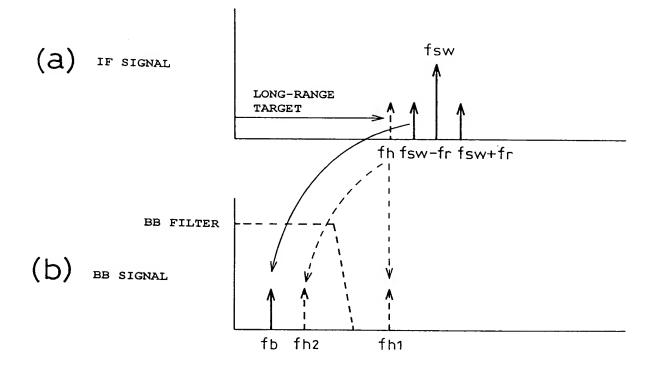
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Fig.7



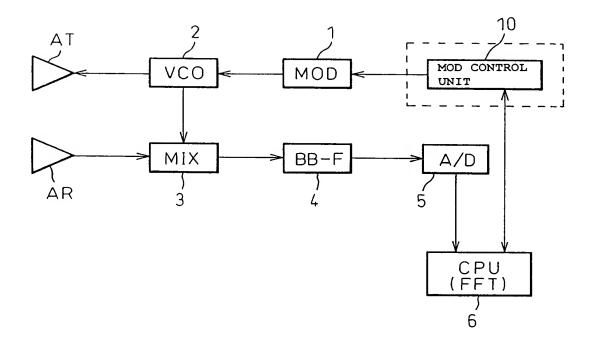
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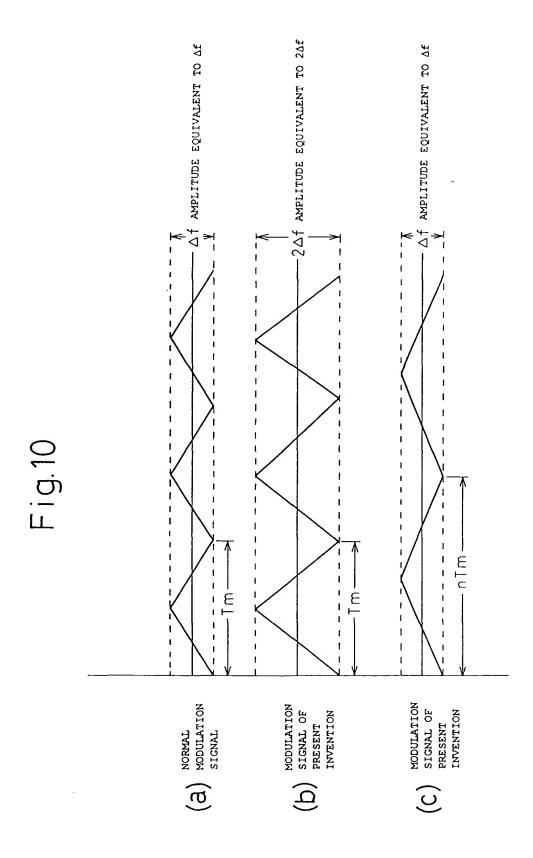
Fig.8



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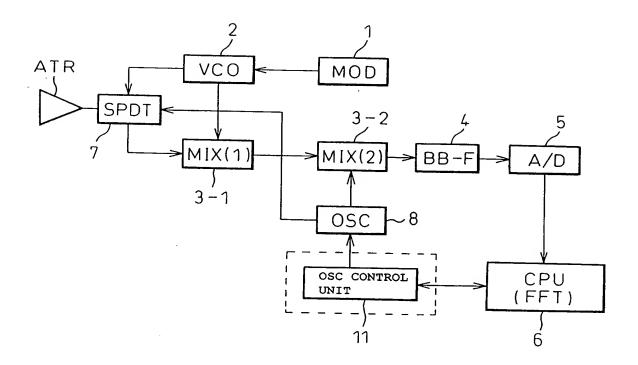
Fig.9





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Fig.11



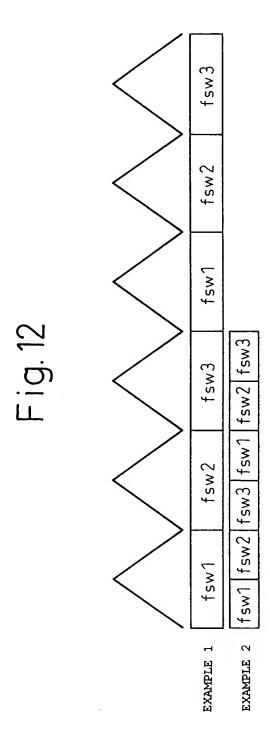
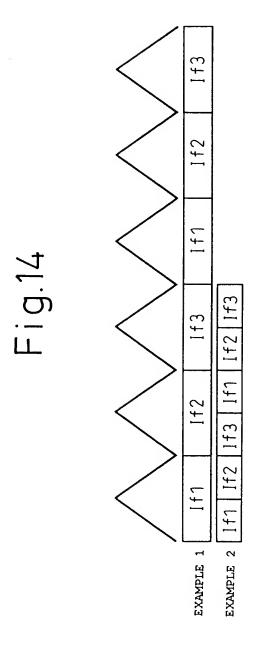


Fig.13 AT fo VCO MOD √fo UP CONVERTER If1 AR 3 - 2fo+If1 MIX(1) MIX(2)BB-F 3-1 OSC OSC CONTROL CPU (FFT) UNIT 11

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Fig.15

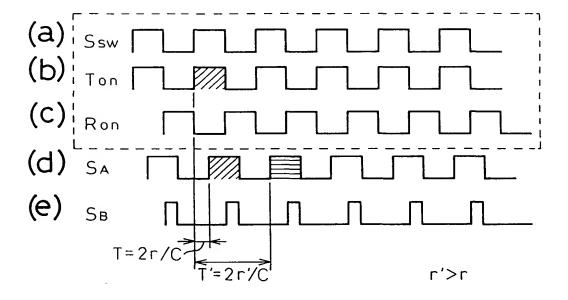
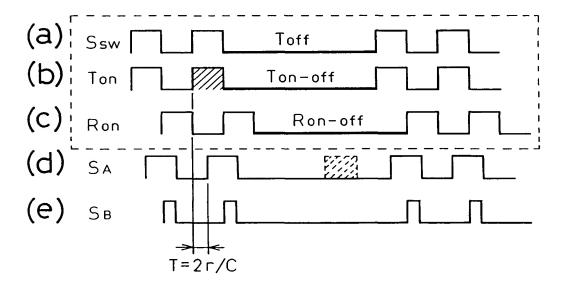


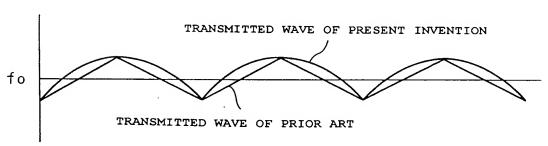
Fig.16

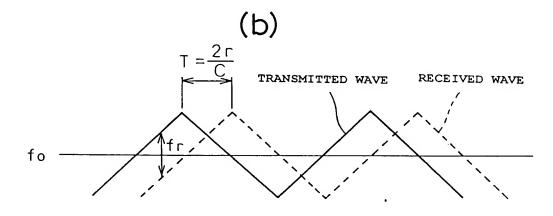


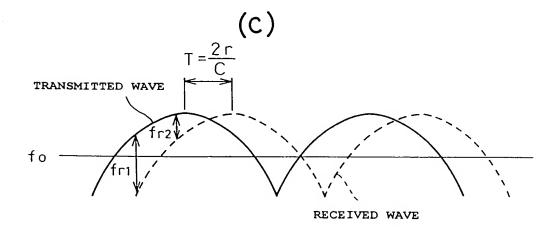
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Fig.17

(a)







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Fig.18

